Substitute Form PTO-1449 (Modified) U.S. Department of Commerce Patent and Trademark Office		Attorney's Docket No. 15670-029US1	Application No. 10/558,842		
Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant Chung-Kuan Cheng, et al.			
		Filing Date November 29, 2005	Group Art Unit	2825 /N.N./	

U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
/N.N./	AA	6,577,992	06//10/03	Tcherniaev et al.			

Foreign Patent Documents or Published Foreign Patent Applications								
Examiner	Desig.	Document	Publication	Country or			Trans	slation
Initial	ID	Number	Date	Patent Office	Class	Subclass	Yes	No
/N.N./	AB	2004/109452	12/16/04	PCT				

	Other D	ocuments (include Author, Title, Date, and Place of Publication)
Examiner Initial	Desig. ID	Document
/N.N./	AC	Black, J.R., "Electromigration Failure Modes in Aluminum Metalization for Semiconductor Devices," Proc. IEEE, pp. 1587-1594, Sept. 1969
/N.N./	AD	Bobba et al., "IC power distribution challenges," IEEE/ACM International Conference on Computer Aided Design, pp. 643-650, (2001)
/N.N./	AE	Brandt, A., "Multi-level adaptive solutions to boundary value problems," Math. Comput., 31: 333-390 (1977)
/N.N./	AF	Briggs, W.L., "A Multigrid Tutorial," SIAM 2000, http://www.llnl.gov/casc/people/henson/mgtut/ps/mgtut.pdf (accessed on 04/06/06), 119 pages
/N.N./	AG	Cao et al, "HiPRIME: Hierarchical and Passivity Reserved Interconnect Macromodeling Engine for RLKC Power Delivery," IEEE/ACM Design Automation Conference, pp. 379-384, (2002)
/N.N./	АН	Chen, H.a nd J. Neely, "Interconnect and circuit modeling techniques for full-chip power supply noise analysis," IEEE Transactions on Components, Packaging, and Manufactured Technology, Part B, Vol. 21, No. 3, pp. 209-215, August 1998
/N.N./	AI	Chen, T. and C. Chen, "Efficient Large-Scale Power Grid Analysis Based on Preconditioned Krylov-Subspace Iterative Methods," IEEE/ACM Design Automation Conference, pp. 559-562, (2001)
/N.N./	AJ	Devgan et al., "How to Efficiently Capture On-Chip Inductance Effects: Introducing a New Circuit Element K.," IEEE/ACM International Conference on Computer Aided Design, pp. 150-155 (November, 2000)
/N.N./	AK	Katopis, G.A., "Delta-I Noise Specification for a High-performance Computing Machine," Proc. Of the IEEE, Vol. 73, pp. 1450-1415, 1985 [Meditech, "Correction to: Katopis, G.A., 'Delta-I Noise Specification for a High-performance Computing Machine,' Proc. Of the IEEE, Vol. 73, pp. 1450-1415, 1985," Proceedings of the IEEE 70(12): 1864 (December, 1985) attached following Katopis article]
/N.N./	AL	Kozhaya et al., "Multigrid-like technique for power grid analysis," IEEE/ACM International Conference on Computer Aided Design, 2001. ICCAD 2001, November 4-8, 2001, San Jose, California, pp. 480-487
/N.N./	AM	Kozhaya et al., "A multigrid-like technique for power grid analysis," IEEE Transactions on Computer-Aided Design of Integrated Circuits, Volume 21, Issue 10, pp. 1148-1160, October 2002

Examiner Signature	Date Considered				
/Nha Nguyen/	08/06/2009				
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.					

. ** IDS filed: 4/21/2006

Sheet <u>2</u> of <u>2</u>

Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 15670-029US1	Application No. 10/558,842	
Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant Chung-Kuan Cheng, et al.		
		Filing Date November 29, 2005	Group Art Unit 2825	

Other Documents (include Author, Title, Date, and Place of Publication)				
Examiner	Desig.			
Initial	ID	Document		
/N.N./	AN	La Scala et al., "A relaxation type multigrid parallel algorithm for power system transient stability analysis," IEEE International Symposium on Circuits and Systems, 1989. May 8-11, 19989, Portland, Oregon, Volume 3, pp. 1954-1957 (1989)		
/N.N./	АО	La Scala, M. and A. Bose, "Relaxation/Newton methods for concurrent time step solution of differential-algebraic equations in power system dynamic simulations," IEEE Transactions on Circuits and Systems 1: Fundamental Theory and Applications, Volume 40, Issue 5, pp. 317-330 (May, 1993)		
/N.N./	AP	Lee, Y. and C. Chen, "Power Grid Transient Simulation in Linear Time Based on Transmission- Line-Modeling Alternating-Direction-Implicit' Methofs," IEEE/ACM International Conference on Computer Aided Design, pp. 75-80, (2001)		
/N.N./	AQ	Lin, S. and N. Chiang, "Challenges in Power-Ground Integrity," IEEE/ACM International Conference on Computer Aided Design, pp. 651-654, (2001)		
/N.N./	AR	Nassif, S.R., "Fast Power Grid Simulation," IEEE/ACM Design Automation Conference, pp. 156-161, (2000)		
/N.N./	AS	Nassif, S.R. and J. Kozhaya, "Multigrid methods for power grid simulation," The 2000 IEEE International Symposium on Circuits and Systems, 2000. May 28-31, 2000, Geneva, Switzerland, Volume 5, pp. 457-460 (2000)		
/N.N./	AT	Stuben, K., "A review of algebraic multigrid," Journal of Computational and Applied Mathemactics, vol. 128 (No. 1-2): 281-309 (March 1, 2001)		
/N.N./	AU	Stuben, K., "Algebraic Multigrid (AMG): An Introduction with Applications," GMD Report No. 70 (November 1999), 127 pages.		
/N.N./	AV	Taylor, S., "The Challenge of Designing Global Signals in UDSM CMOS," IEEE Custom Integrated Circuits Conference, San Diego, CA, pp. 429-435, (1999)		
/N.N./	AW	Wang, K. and M. Marek-Sadowska, "Power/ground mesh area optimization using multigrid-based techniques [IC design]," Design, Automation and Test in Europe Conferences and Exhibition, 2003, Santa Barbara, CA, pp. 850-855 (March 3-7, 2003)		
/N.N./	AX	Zhao et al., "Frequency domain analysis of switching noise on power supply network," IEEE/ACM International Conference on Computer Aided Design, pp. 487-492 (2000).		
/N.N./	AY	Zhao et al., "Hierarchical analysis of power distribution networks," IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems," Vol. 21, No.2, IEEE, pp. 159-168, Feb. 2002		
/N.N./	AZ	Zhu et al., "Power network analysis using an adaptive algebraic multigrad approach," Proceedings of the Design Automation Conference, 2003, San Diego, California, June 2-6, 2003, pp. 105-108		

Examiner Signature	Date Considered				
/Nha Nguyen/	08/06/2009				
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with					